



03/06/03 6469-61238 174729

2816
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re application of: Agoston et al.

Art Unit: 2816

Application No. 10/053,529

Filed: November 2, 2001

For: ULTRAFast SAMPLER WITH NON-
PARALLEL SHOCKLINE

Examiner:

Date: March 6, 2003

CERTIFICATE OF MAILING

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being deposited with the United States Postal Service on March 6, 2003 as First Class Mail in an envelope addressed to: COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231.

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Attorney for Applicant

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INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. § 1.97(b)(3)

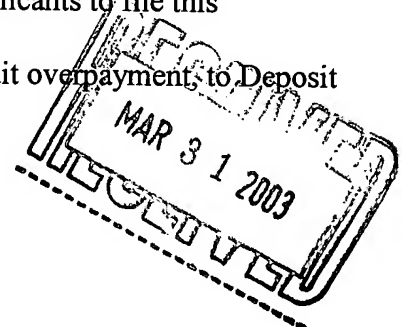
COMMISSIONER FOR PATENTS
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Sir:

Listed on the accompanying form PTO-1449 and enclosed herewith are several English-language documents. Applicants respectfully request that these documents be listed as references cited on the issued patent.

Applicants filed this Information Disclosure Statement ("IDS") before the mailing date of a first Office action on the merits. As a result, no fee should be required to file this IDS.

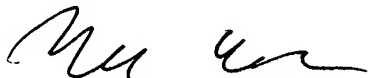
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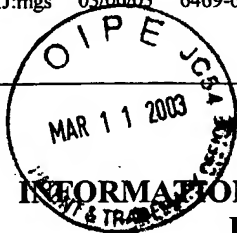
Account No. 02-4550. A **duplicate** copy of this Information Disclosure Statement is enclosed.

Respectfully submitted,

KLARQUIST SPARKMAN, LLP

By 
Michael D. Jones
Registration No. 41,879

One World Trade Center, Suite 1600
121 S.W. Salmon Street
Portland, Oregon 97204
Telephone: (503) 226-7391
Facsimile: (503) 228-9446



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Attorney Docket Number 6469-61238

Application Number 10/053,529

Filing Date November 2, 2001

First Named Inventor Agoston

Art Unit 2816

Examiner Name --

**Examiner's
Initials***

**Cite No.
(optional)**

OTHER DOCUMENTS

S. Allen, "Schottky Diode Integrated Circuits for Sub-Millimeter-Wave Applications," University of California (June 28, 1994).

M. Case, "Nonlinear Transmission Lines for Picosecond Pulse, Impulse and Millimeter-Wave Harmonic Generation," University of California (July 2, 1993).

S.T. Allen et al., "725 GHz Sampling Circuits Integrated with Nonlinear Transmission Lines," IEEE Device Research Conference (1994).

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**DATE
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